

DERWENT-ACC-NO: 1985-071212

DERWENT-WEEK: 198512

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TITLE: Resist pattern prodn. - by lithographic printing of  
circuit board, pinholes overprinting, and curing

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PRIORITY-DATA: 1983JP-0131886 (July 21, 1983)

PATENT-FAMILY:

PUB-NO	PUB-DATE	LANGUAGE	PAGES	MAIN-IPC
JP 60024990 A	February 7, 1985	N/A	006	N/A
JP 90052634 B	November 14, 1990	N/A	000	N/A

APPLICATION-DATA:

PUB-NO	APPL-DESCRIPTOR	APPL-NO	APPL-DATE
JP 60024990A	N/A	1983JP-0131886	July 21, 1983
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ABSTRACTED-PUB-NO: JP 60024990A

BASIC-ABSTRACT:

Resist pattern making method comprises (1) lithographic printing on the circuit board using resist ink which is curable by heat or active energy beam, (2) same pattern printing on the same position to remove pinholes and (3) curing printed pattern by exposing to heat or active energy beam. Pref. (2') standing of printed pattern is imposed between (2) and (3).

Conventional resist pattern making method is silk screen printing and by this method pattern resolution is 150-200 micron line width, so it cannot keep-up with recent patterns miniturisation. Lithographic printing can provide high resolution pattern of 30 micron line width, but has disadvantage of pinholes.

The improved lithographic printing method comprises process (1), (2), (2') and (3).

USE/ADVANTAGE - This resist pattern making method provides cheap and high resolution pattern.

CHOSEN-DRAWING: Dwg.0/0

TITLE-TERMS: RESIST PATTERN PRODUCE LITHO PRINT CIRCUIT BOARD  
PINHOLE OVERPRINT  
CURE

DERWENT-CLASS: G06 L03 P75 P83 V04

CPI-CODES: G06-D06; G06-E04; L03-D03B; L03-H04E2;

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